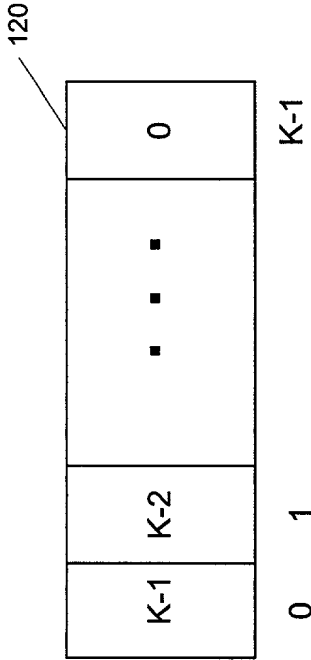
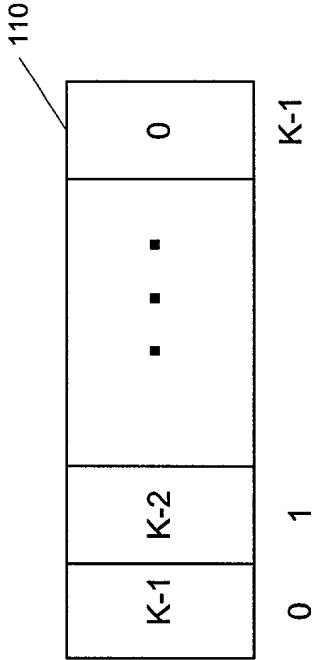


Future State
Pointers



Architectural State
Pointers



Reference
Counters

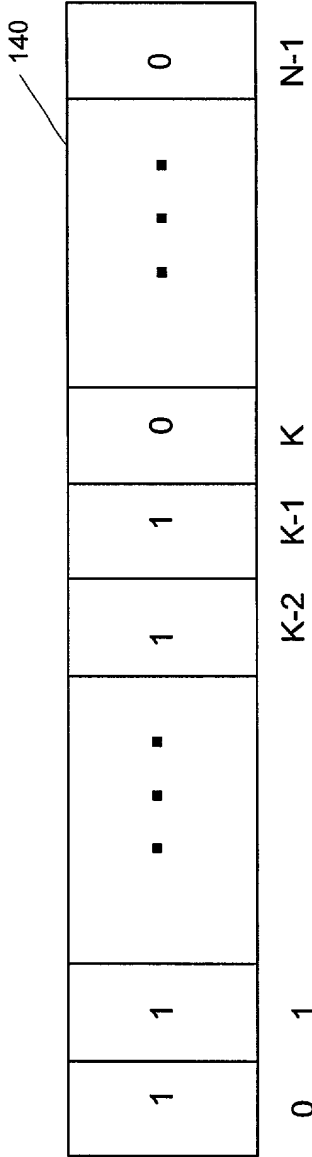
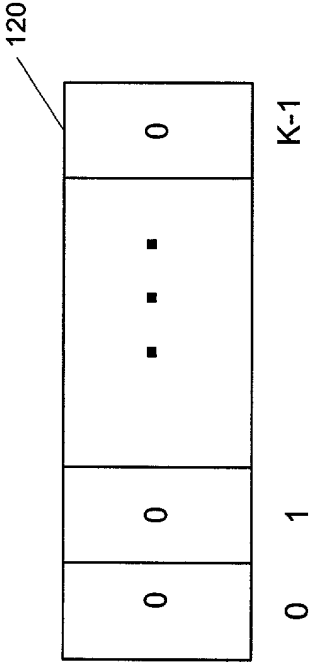
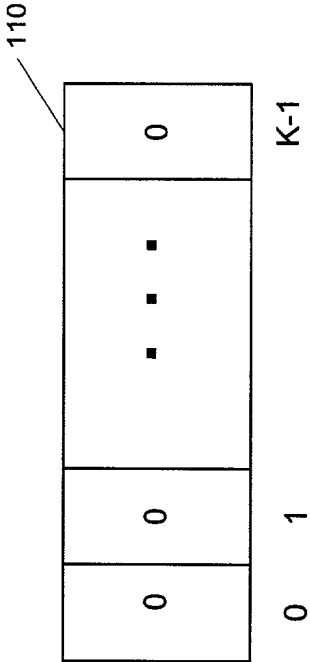


Figure 2

Future State
Pointers



Architectural State
Pointers



Reference
Counters

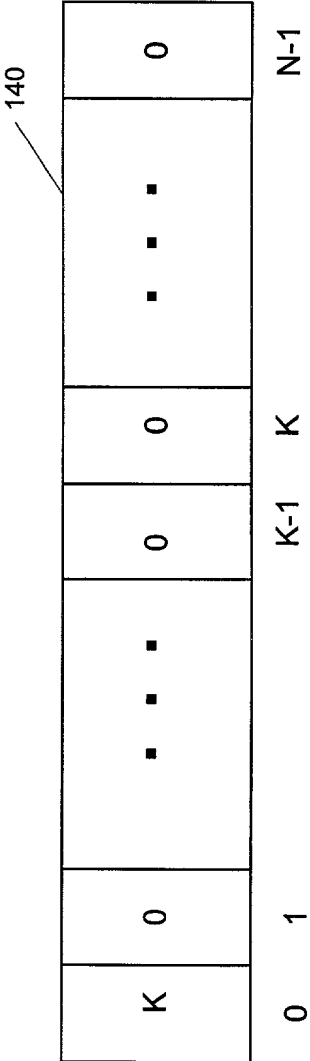
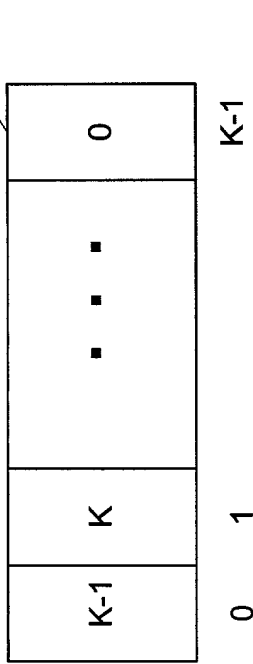


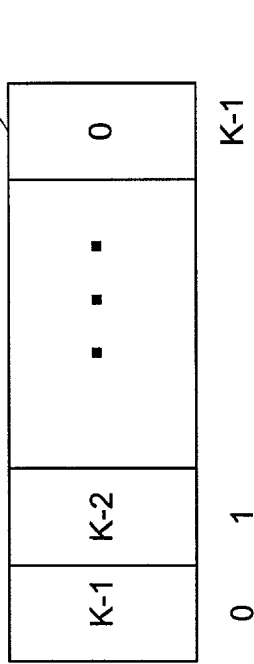
Figure 3

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Future State
Pointers



Architectural State
Pointers



Reference
Counters

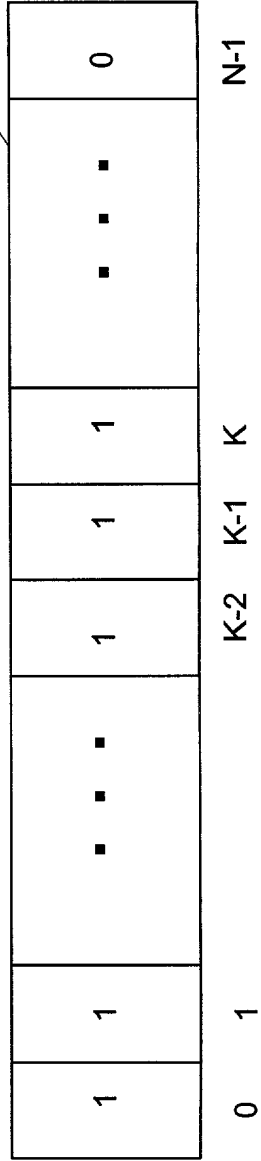
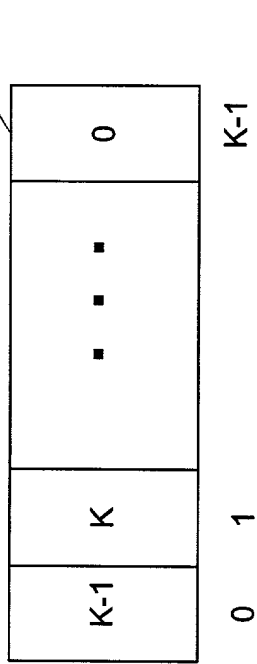


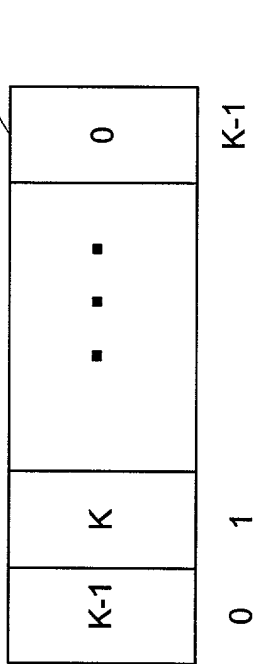
Figure 4

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Future State
Pointers



Architectural State
Pointers



Reference
Counters

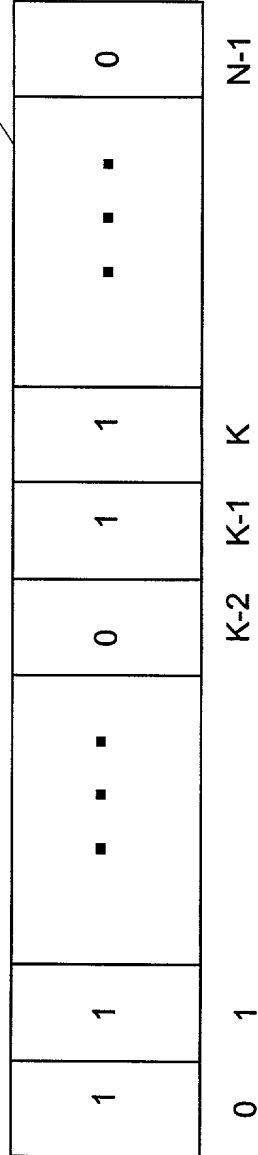
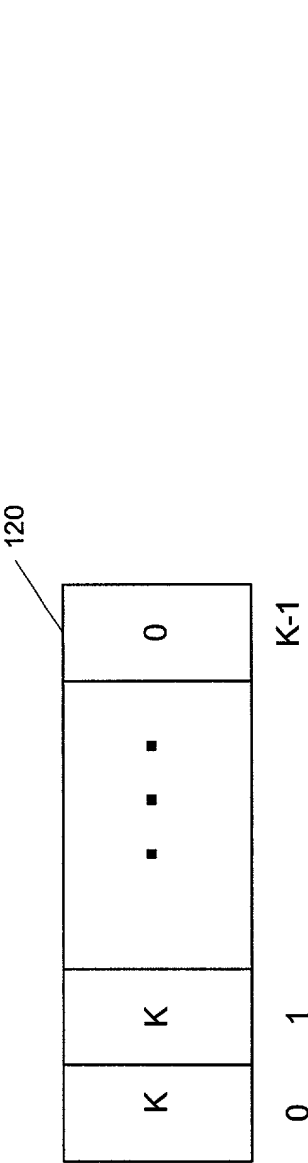
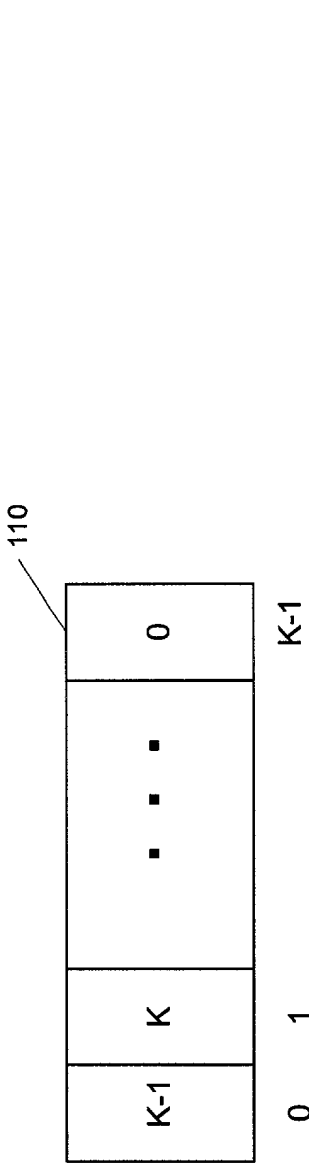


Figure 5

Future State
Pointers



Architectural State
Pointers



Reference
Counters

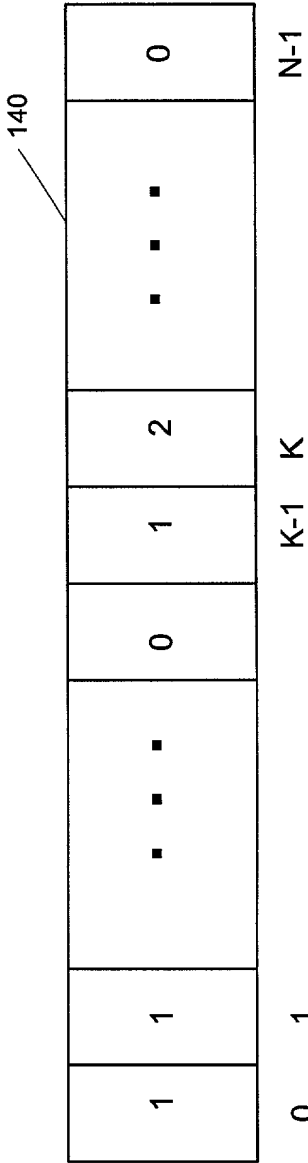
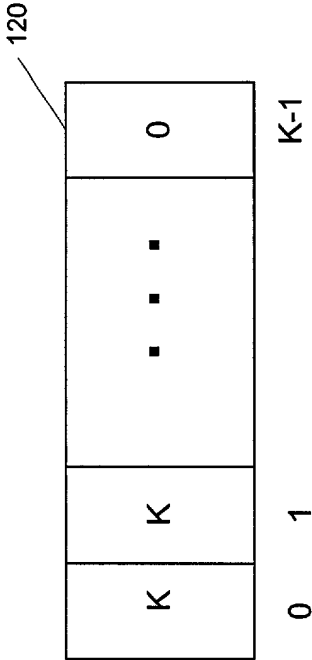
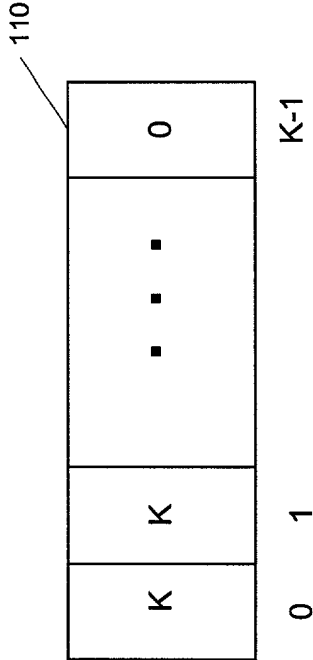


Figure 6

Future State
Pointers



Architectural State
Pointers



Reference
Counters

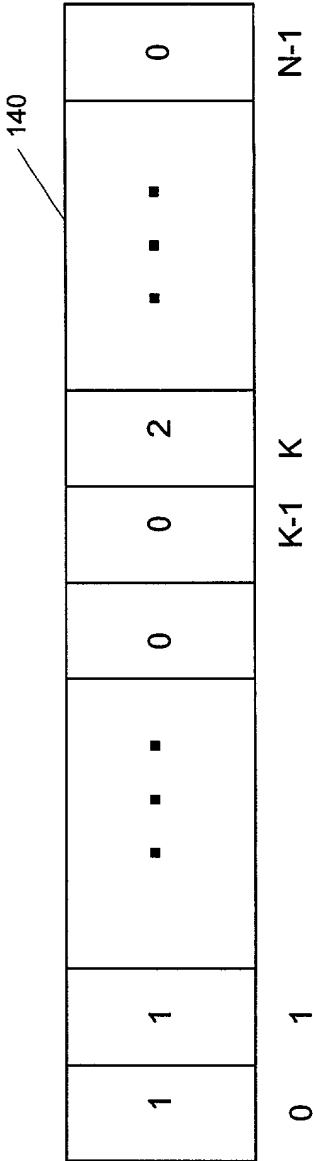


Figure 7

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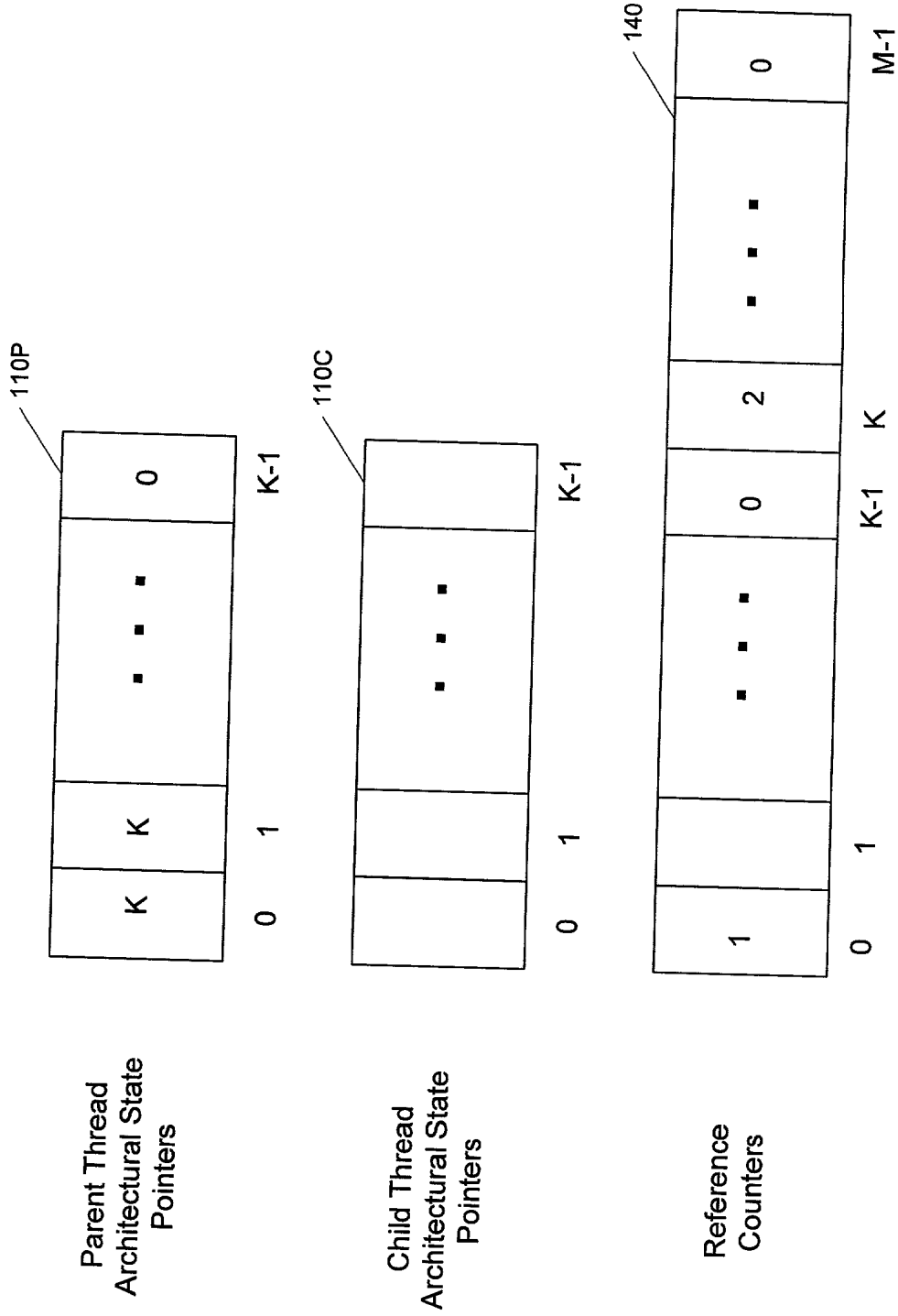
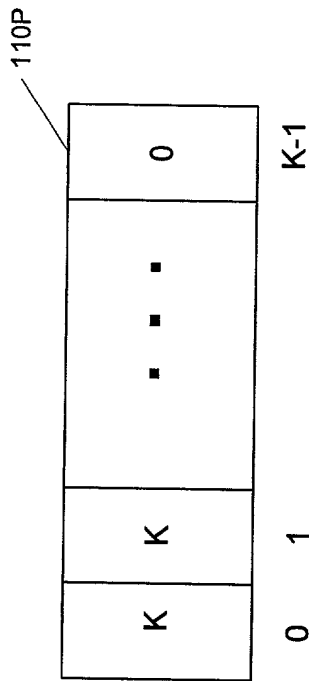


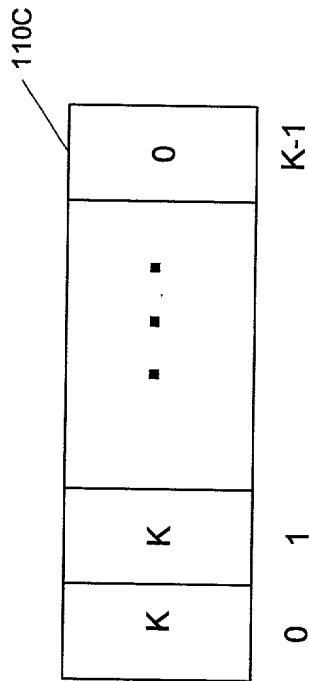
Figure 8

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Parent Thread
Architectural State
Pointers



Child Thread
Architectural State
Pointers



Reference
Counters

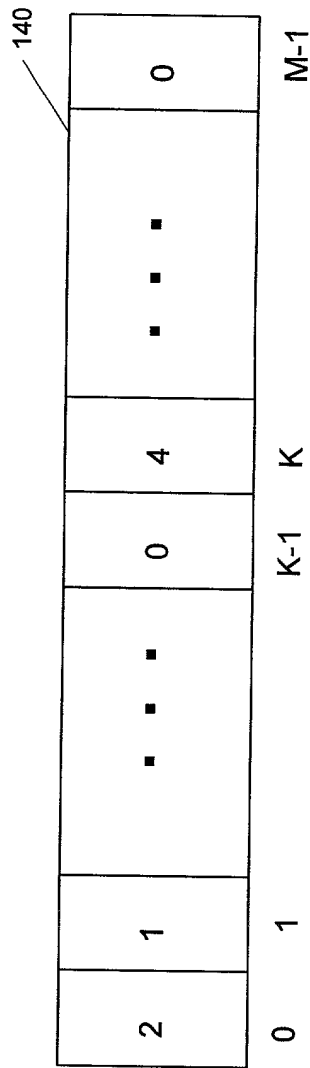


Figure 9